

Features

- Uses PingWei advanced PerfectMOS6 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- Excellent Low Ciss
- Qualified according to JEDEC criteria



100% DVDS Tested
100% Avalanche Tested

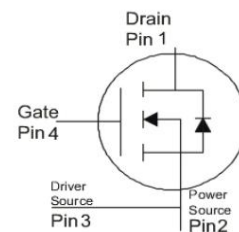
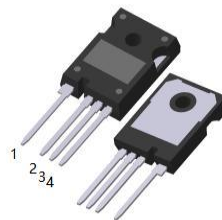
Applications

- PFC stages, hard switching PWM stages and resonant switching
- PWM stages for e.g. PC Silverbox, Adapter, LCD & PDP TV, Lighting, Server, Telecom and UPS

Product Summary

V_{DS}	650V
$R_{DS(on)@10V}$ typ	74mΩ
I_D	41A

TO-247-4L



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PWE080N65SFY4	PWE080N65SFY4	TO-247-4L	Tube	N/A	N/A	30pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current	I_D	41	A
$T_C = 25^\circ\text{C}$ (Silicon limit)		120	
$T_C = 25^\circ\text{C}$ (Package limit)		26	
$T_C = 100^\circ\text{C}$ (Silicon limit)		3	
$T_a = 25^\circ\text{C}$			
Pulsed drain current ($T_C = 25^\circ\text{C}$, $t_p = 100\mu\text{s}$)	$I_{D\ pulse}$	166	A
Avalanche energy, single pulse ($V_{ds}=100\text{V}$)	E_{AS}	750	mJ
Gate-Source voltage	V_{GS}	± 20	V
Power dissipation	P_{tot}	260	W
$T_C = 25^\circ\text{C}$		2.1	
$T_a = 25^\circ\text{C}$			
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	T_{sold}	260	$^\circ\text{C}$



Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	RthJC	-	0.37	0.48	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	RthJA	-	-	60	°C/W	-

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	3.0	-	5.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}	-	0.7	10	μA	$V_{DS}=650V, V_{GS}=0V$ $T_j=25^\circ C$ $T_j=150^\circ C$
Gate-source leakage current	I_{GSS}	-	± 10	± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	74	80	mΩ	$V_{GS}=10V, I_D=20A$
Transconductance	g_{fs}	-	29	-	S	$V_{DS}=5V, I_D=20A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	3539	-	pF	$V_{GS}=0V, V_{DS}=400V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	88	-		
Reverse Transfer Capacitance	C_{rss}	-	5	-		
Gate Total Charge	Q_G	-	91	-	nC	$V_{DS}=325V, I_D=23.5A,$ $V_{GS}=10V$
Gate-Source charge	Q_{gs}	-	28	-		
Gate-Drain charge	Q_{gd}	-	42	-		
Turn-on delay time	$t_{d(on)}$	-	40	-	ns	$V_{GS}=10V, V_{DD}=235V,$ $R_{G_ext}=2.5\Omega, I_D=47A$
Rise time	t_r	-	87	-		
Turn-off delay time	$t_{d(off)}$	-	109	-		
Fall time	t_f	-	36	-		
Gate resistance	R_G	-	11	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$



Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.88	1.5	V	$V_{GS}=0V, I_{SD}=20A$
Body Diode Continuous Forward Current	I_S	-	-	41	A	$TC = 25^{\circ}C$
Body Diode Pulsed Current	I_S pulse	-	-	166	A	$TC = 25^{\circ}C$
Body Diode Reverse Recovery Time	t_{rr}	-	150	-	ns	$V_R=37V, I_F=10A,$ $dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	Q_{rr}	-	1055	-	nC	



Typical Performance Characteristics

Fig 1: Output Characteristics

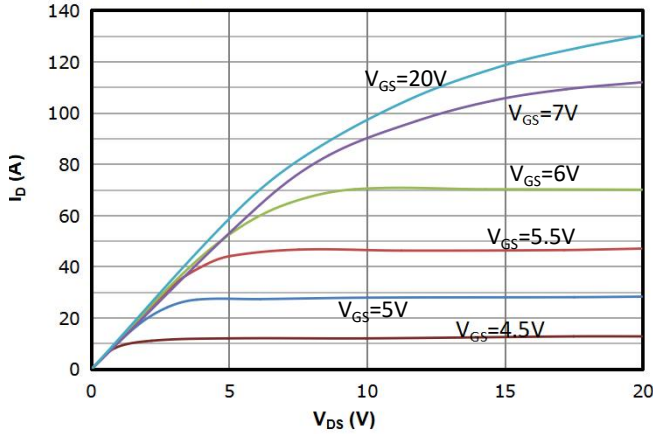


Fig 2: Transfer Characteristics

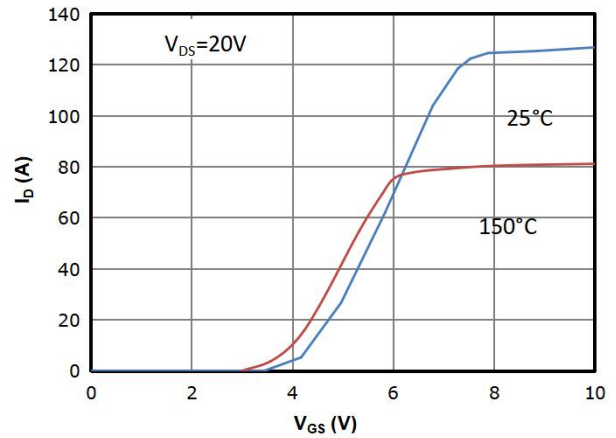


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

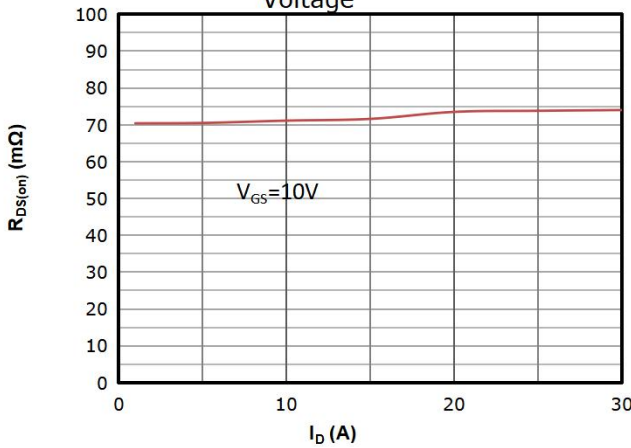


Fig 4: $R_{DS(on)}$ vs Gate Voltage

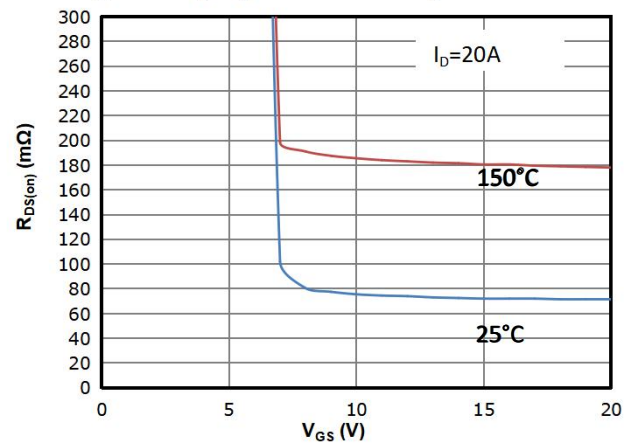


Fig 5: $R_{DS(on)}$ vs. Temperature

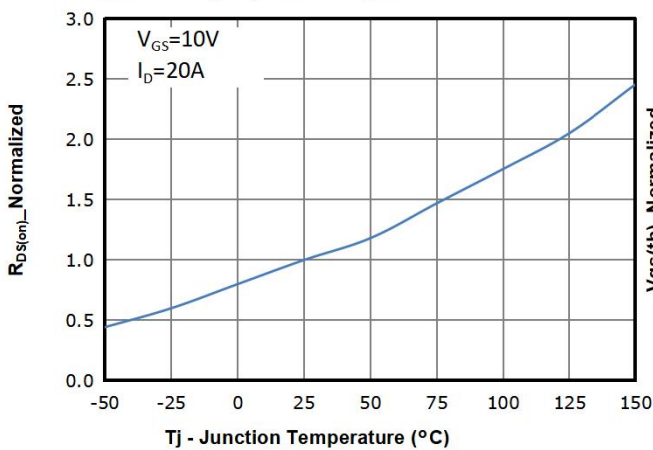


Fig 6: $V_{GS(th)}$ vs. Temperature

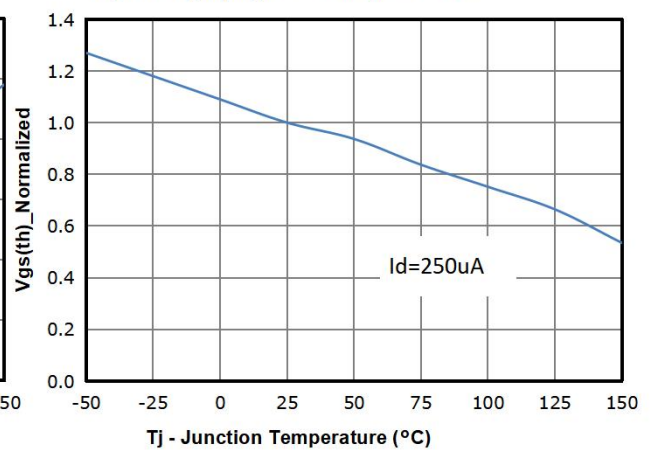




Fig 7: BVdss vs. Temperature

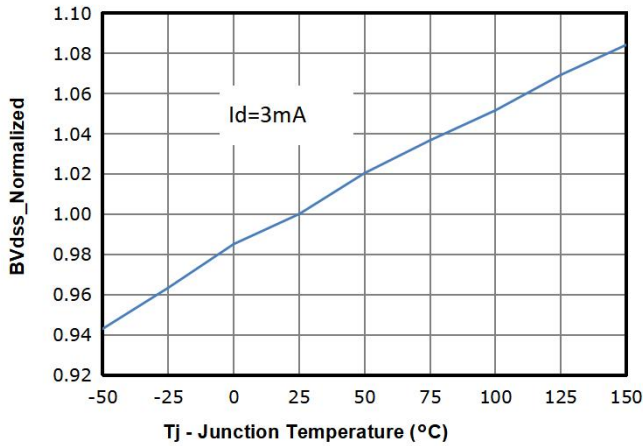


Fig 8: Capacitance Characteristics

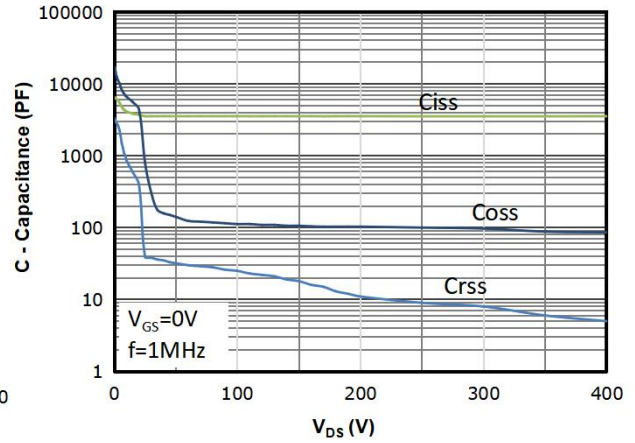


Fig 9: Gate Charge Characteristics

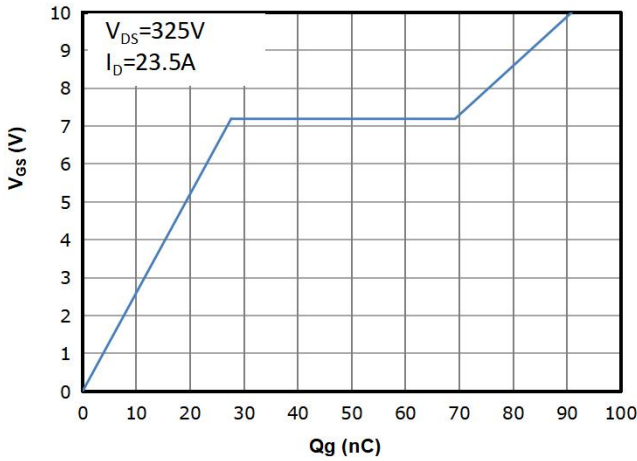


Fig 10: Body-diode Forward Characteristics

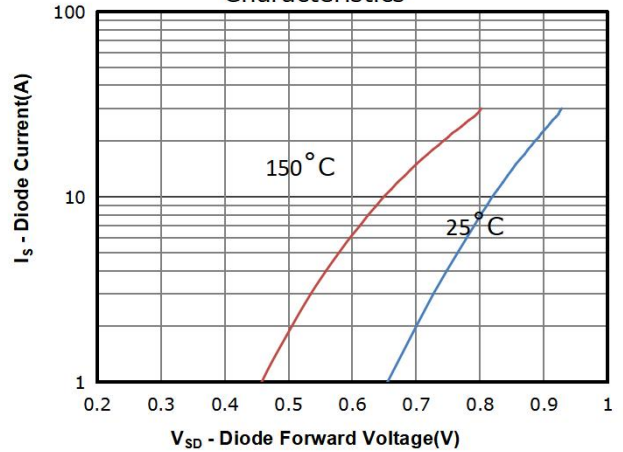


Fig 11: Power Dissipation

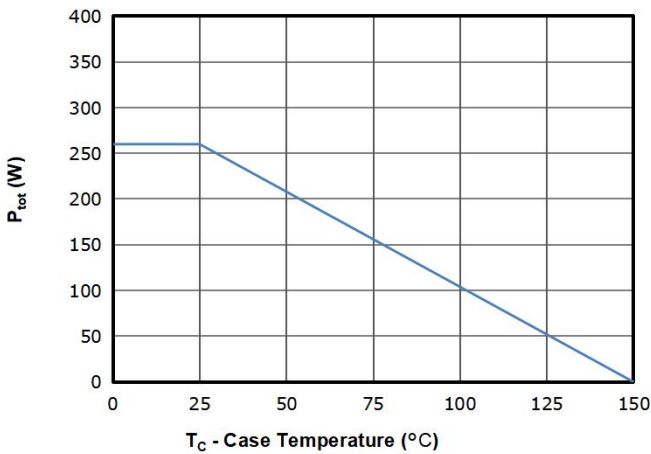


Fig 12: Drain Current Derating

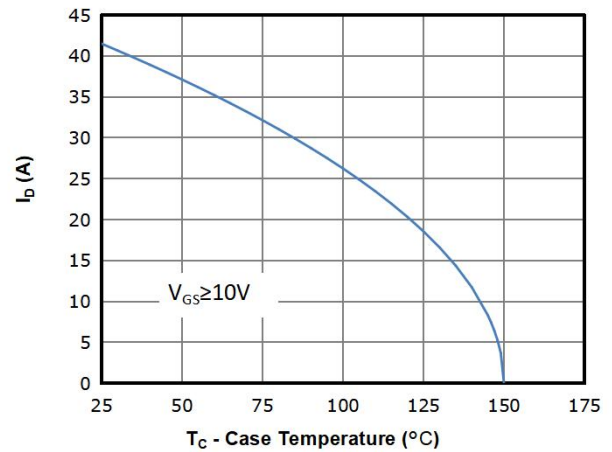




Fig 13: Safe Operating Area

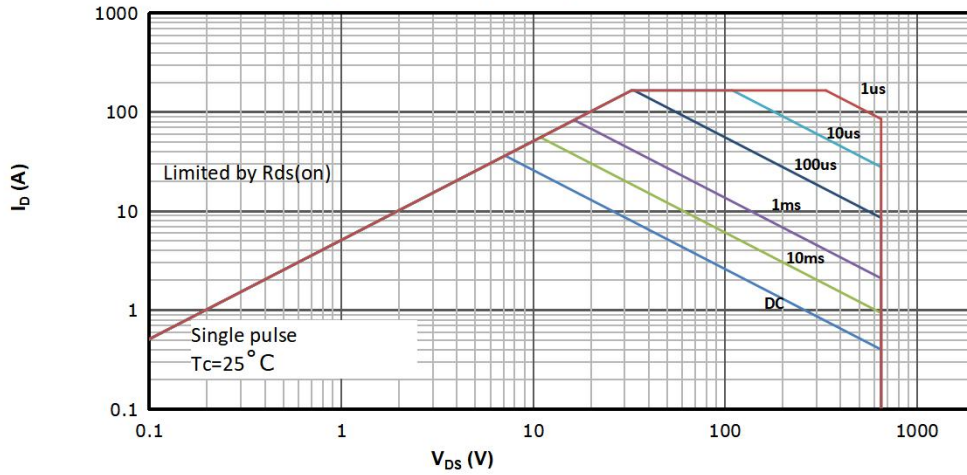
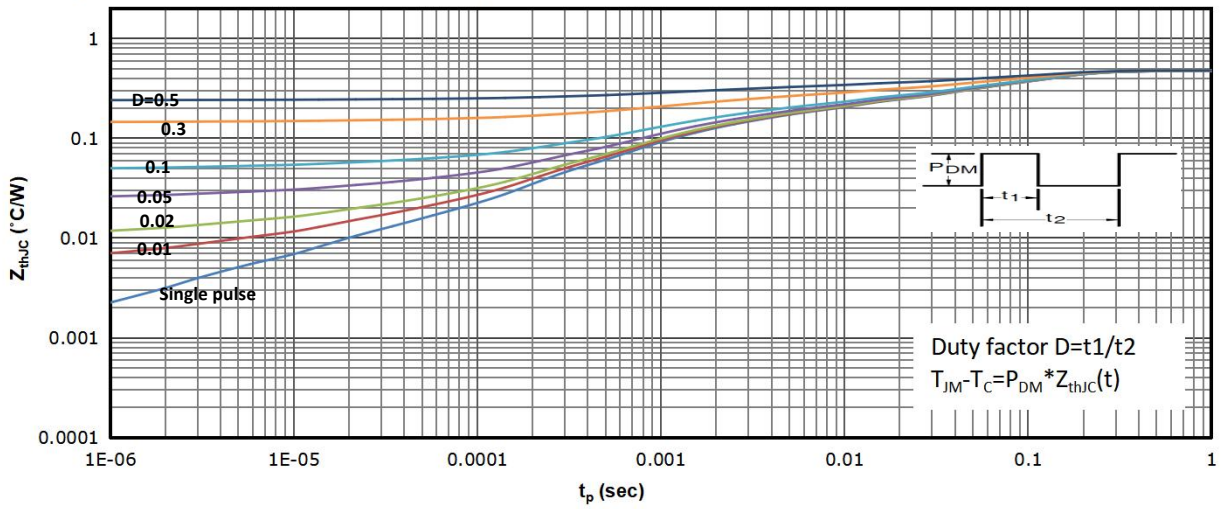
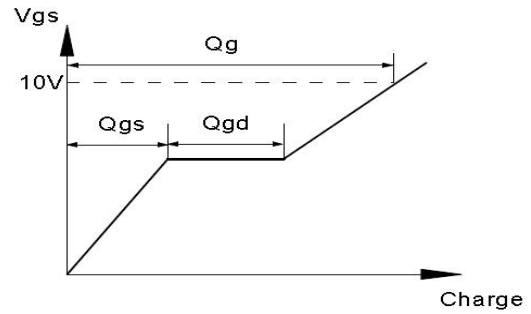
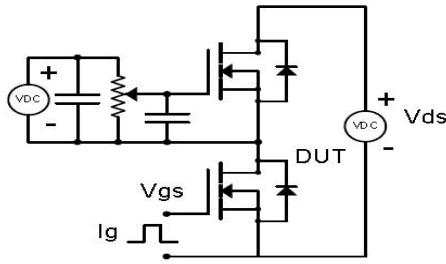


Fig 14: Max. Transient Thermal Impedance

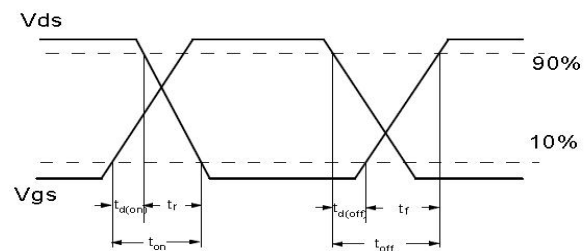
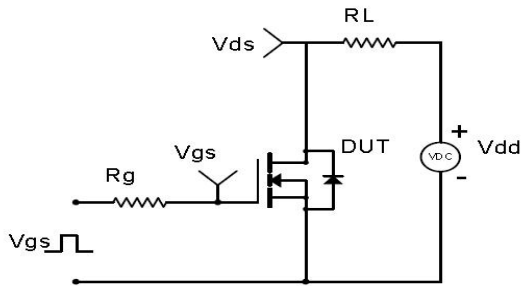


Test Circuit & Waveform

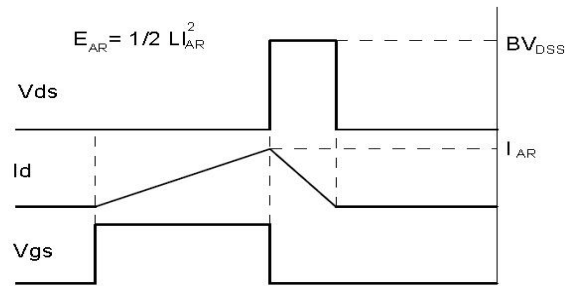
Gate Charge Test Circuit & Waveform



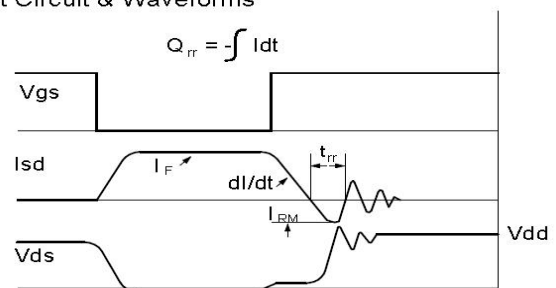
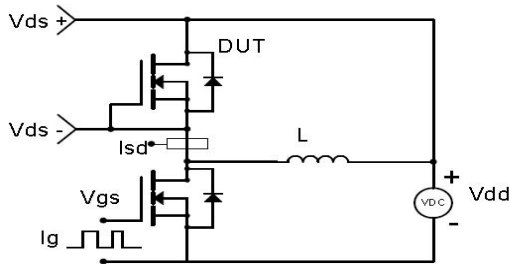
Resistive Switching Test Circuit & Waveforms



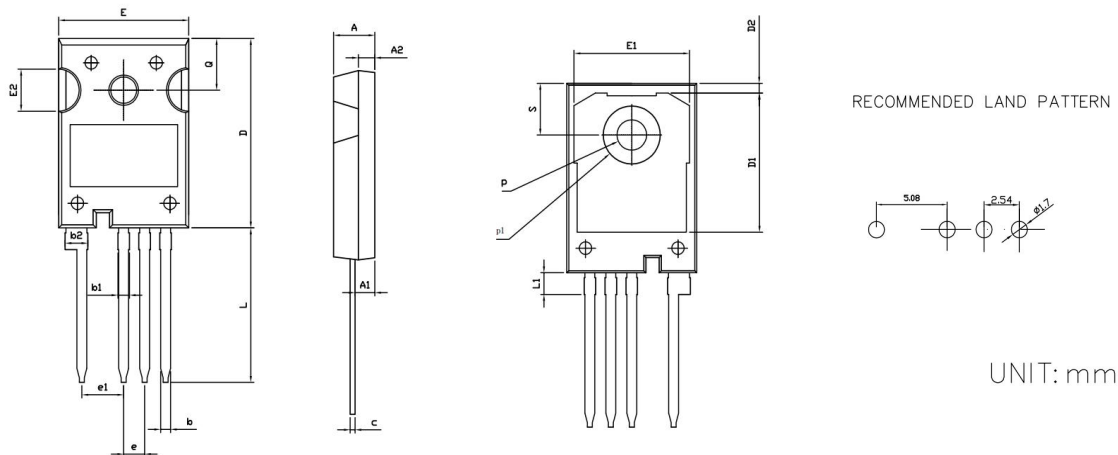
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TO-247-4L



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.20	0.189	0.205
A1	2.25	2.45	0.089	0.096
A2	1.85	2.15	0.073	0.085
b	1.05	1.35	0.041	0.053
b1	1.00	1.60	0.039	0.063
b2	2.35	2.95	0.093	0.116
c	0.50	0.70	0.020	0.028
D	22.34	22.74	0.880	0.895
D1	16.00	17.00	0.630	0.669
D2	0.97	1.37	0.038	0.054
e	2.34	2.74	0.092	0.108
e1	4.88	5.28	0.192	0.208
E	15.60	16.00	0.614	0.630
E1	13.50	14.50	0.531	0.571
E2	4.80	5.20	0.189	0.205
L	18.08	18.68	0.712	0.735
L1	2.38	2.78	0.094	0.109
p	3.50	3.70	0.138	0.146
p1	6.60	7.00	0.260	0.276
Q	6.00	6.30	0.236	0.248
S	6.00	6.30	0.236	0.248



Revision History

Revision	Date	Major changes
1.0	2023/1/31	Release of Formal Version.

Disclaimer

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

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